

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Gustavson, et al.

Serial No.: 10/671,935

Group Art Unit: 2124

Filing Date: September 29, 2003

Examiner: Unknown

**For:** METHOD AND STRUCTURE FOR PRODUCING HIGH PERFORMANCE LINEAR ALGEBRA ROUTINES USING A SELECTABLE ONE OF SIX POSSIBLE LEVEL 3 L1 KERNEL ROUTINES

Honorable Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following document listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. A copy of the listed documents are provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Assignee's Deposit Account No. 50-0510.

Respectfully submitted,



Frederick E. Cooperrider  
Registration No. 36,769

Date:

12/30/03

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# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

YOE920030330US1

Application Number

10/671,935

Applicant(s)

Gustavson, et al.

Filing Date

September 29, 2003

Group Art Unit

2124

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

## FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Juan J. Navarro, Elena Garcia, and Josep R. Herrero, "Data Prefetching and Multilevel Blocking for Linear Algebra Operations" in International Conference on Supercomputing (ICS'96), pp. 109-116, May 1996.
		John A. Gunnels, Greg M. Henry, and Robert A. van de Geijn, "A Family of High-Performance Matrix Multiplication Algorithms," ICCS 2001, LNCS 2073, pp. 51-60, 2001 (copyright Springer-Verlag Berlin Heidelberg 2001).

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.